## IDENTIFICATION

PRODUCT CODE: MAINDEC-8E-D1G8-D

PRODUCT NAMEL POP-8E MEMORY POWER ON/OFF TEST

DATE CREATED: JUNE 11, 1971

MAINTAINER: DIAGNOSTIC GROUP

AUTHOR: BRUCE HANSEN

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1. ABSTRACT
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THIS PROGRAM IS A MEMORY DATA VALIDITY TEST TO BE USED AFTER A SIMULATED POWER FAIL.

2 REQUIREMENTS

EQUIPMENT

POP-BE EQUIPPED WITH TELETYPE

STORAGE

MEMORY LOCATIONS 0000(8) - - 7601(8)

BINARY LOADER AND RIM MUST BE STORED IN LAST MEMORY PAGE.

3. USAGE

3.1 LOADING

NORMAL BINARY TAPE LOADING PROCEDURES

3,2 START UP AND/OR ENTRY

LOAD ADDRESS 0200, PRESS CLEAR AND THEN CONT. THE PROGRAM SHOULD THEN HALT AT LOCATION 0031(8). LOAD ADDRESS 0201, PRESS CLEAR AND THEN CONT. THE PROGRAM SHOULD NOW LOOP.

3.3 ERRORS IN USAGE

ERRORS DETECTED BY THE PROGRAM CAUSE THE PROGRAM TO HALT AT MEMORY LOCATION 0047(8). THE CONTENTS OF MEMORY ADDRESS 0011(8) AND 0012(8) INDICATE THE ADDRESSES OF THE DATA THAT FAILED TO CHECK-SUM. MEMORY ADDRESSES 0007(8) AND 0010(8) CONTAIN THE DATA WORDS THAT FAILED TO CHECK-SUM.

LOWER ADDRESS = (0011(8)) = 202(8)-4001(8)

UPPER ADDRESS = (0012(8)) = 4002(8) - 7601(8)

LOWER ERROR WORD = (0007(8)) = 2525(8)

UPPER ERROR WORD = (0010(8)) = 5252(8)

3.4 ERROR RECOVERY

PRESS CONTINUE TO TEST FOR OTHER ERROR WORDS IN MEMORY. TO RELOAD MEMORY DO 3.2.

THIS PROGRAM TESTS MEMORY FOR BIT DROP OUT AND PICK UP AFTER A SIMULATED POWER FAILURE HAS OCCURRED.

BY STARTING THE PROGRAM AT MEMORY ADDRESS 200(8). DATA WORDS CONSISTING OF 2525(8) ARE WRITTEN INTO MEMORY LOCATIONS 0202(8) - - 4001(8), AND DATA WORDS CONSISTING OF 5252(8) ARE WRITTEN INTO MEMORY LOCATIONS 4002(8) - - 7601(8) AFTER WHICH THE PROGRAM HALTS AT MEMORY ADDRESS 0031(8). LOAD ADDRESS 0201(8) AND RESTART THE PROGRAM; THE PROGRAM WILL 2'S ADD THE CONTENTS OF MEMORY LOCATION 202(8) WITH 4002(8). IF THE RESULTS EQUALS 7777(8) THE PROGRAM WILL 2'S ADD THE CONTENTS OF MEMORY LOCATIONS 203(8) WITH 4003(8) ETC. UNTIL THE MEMORY ADDRESSES OF 4001(8) AND 7601(8) ARE TESTED. THE PROGRAM STAYS IN THE 2'S ADD COMPARE LOOP UNTIL AN ERROR OCCURS. CONCURRENTLY CYCLE THE POWER TO THE PDP-8E OFF AND ON. AFTER THE POWER HAS BEEN REAPPLIES TO THE PDP-SE, LOAD ADDRESS 201(8) AND PRESS CLEAR AND THEN CONTINUE. IF AN ERROR OCCURRED DURING THE POWER CYCLING, THE PROGRAM HALTS AT LOCATION ØØ47(8). THE PROGRAM MAY BE RESTARTED AT MEMORY ADDRESS 0201(8) AS MANY TIMES AS DESIRED. RESTART ADDRESS TO FILL MEMORY WITH DATA WORDS IS \$2\$\$(8).

4.2 EXAMPLES AND/OR APPLICATIONS

A HALT OCCURS AT MEMORY ADDRESS 0047(8).

ADDRESS 007(8) = 2535 (DATA WORD)

ADDRESS #010(8) # 5252 (DATA WORD)

ADDRESS 0011(8) = 0270 (ADDRESS WORD)

ADDRESS 0012(8) = 4070 (ADDRESS WORD)

BIT 8 WAS PICKED UP AT MEMORY ADDRESS \$27\$(8).

5. EXECUTION TIME

1 MSEC/LOOP

```
IEMORY POWER ON OFF TEST PAL18
                                                                           PAGE 1
                                                                  15:49
               /PDP 8E MEMORY POWER ON OFF TEST
       0200
               *200
                        JMP START
       5013
 0200
       5032
                        JMP COMPAR
 0201
       0000
                40
 2000
       0000
 0001
       5001
                        JMP 1
       6002
 0002
0003
       0003
       0000
 0004
       0000
 0005
 0006
       0000
               UPPER.
 8087
       0000
       0000
               LOWER,
 0010
       0000
 0011
               LOWM,
       0000
0012
               HIGH,
       7300
                                       /START INITIAL
 0013
                START,
                        CLA CLL
       1056
                        TAD KØ201
 0014
       3011
                        DCA LOWM
 0015
 9916
       1057
                        TAD K4001
 0017
       3012
                        DCA HIGH
       1060
                WRKON,
                        TAD UPREG
 0020
       3411
                        DCA I LOWM
 0021
       1061
                        TAD LOREG
 0022
 0023
       3412
                        DCA I HIGH
       1011
                        TAD LOWM
 0024
 0025
       7041
                        CIA
 8026
       1057
                        TAD K4001
 0027
       7640
                        SEA CLA
 ##30
       5020
                        JMP WRKON
                                         /TURN POWER ON OFF
       7482
                STEND,
                        HLT
 6031
       7300
                COMPAR, CLA CLL
 0032
       1056
 5033
                        TAD K0201
       3011
 0034
                        DCA LOWM
 0035
       1057
                        TAD K4001
       3012
 0036
                        DCA HIGH
                                         /LOWM=UPPER ADDRESS 202-4001
       1411
                        TAD I LOWM
 0037
       3007
                        DCA UPPER
 0040
                                         /HIGH=LOWER ADDRESS 4002-7601
 5041
       1412
                        TAD I HIGH
 5042
       3010
                        DCA LOWER
 0043
       1007
                        TAD UPPER
       1010
 0044
                        TAD LOWER
       7040
 8045
                        CMA
       7440
                        SZA
 0046
       7402
                                        /ERROR, DOES NOT COMPARE
 0047
                        HLT
       1011
 0050
                        TAD LOWM
       7041
 0051
                        CIA
 0052
       1057
                        TAD K4001
      764Ø
 0053
                        SZA CLA
                                        /RETURN TO CHECK MEMORY
 0054
       5037
                        JMP COMPAR+5
 0055
       5032
                        JMP COMPAR
                                        /END OF TEST AREA GO TO SETUP
 0056
       0201
               KØ2Ø1.
                        0201
       4001
               K4001.
 0057
                        4001
       2525
                                       /ERROR WORD 2525
 8868
               UPREG.
```

/PDP 8E MEMORY POWER ON OFF TEST PAL18 V141 9-JUN-71 15:49 PAGE 1-1 0061 5252 LOREG.5252 /ERROR WORD 5252